



#13/EOT(3)
6/10/03
C Moore

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Seiichi MORI

SERIAL NO: 09/876,019

GAU: 2814

FILED: June 8, 2001

EXAMINER: Hoai V. PHAM

FOR: SEMICONDUCTOR MEMORY INTEGRATED CIRCUIT AND ITS MANUFACTURING METHOD

REQUEST FOR EXTENSION OF TIME
UNDER 37 C.F.R. 1.136

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

It is hereby requested that a **three** month extension of time be granted to May 29, 2003 for

- ☒ filing a response to the Official Action dated: November 29, 2002
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

The required fee of \$930.00 is enclosed herewith by check and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

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930.00 00P

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Carl E. Schlier

Eckhard H. Kuesters

Registration No. 28,870



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Tel. (703) 413-3000
Fax. (703) 413-2220
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Carl E. Schlier

Registration No. 34,426

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